

IKONAS GRAPHICS SYSTEMS INC

403 GLENWOOD AVE — RALEIGH NC 27603 — 919/833-5401

SYSTEM CONFIGURATION GUIDE

The flexible, expandable nature of IKONAS graphics systems makes a multitude of system configurations possible. The modular design of IKONAS products makes it possible to begin with a small frame buffer system and later to add more image memory and processor elements. The following system configuration guide lists those modules which are essential to a frame buffer system, those which are essential for a system including a processor, and those which may be added to the basic systems.

BASIC FRAME BUFFER CONFIGURATION:

	<u>Module Name</u>
Image Memory	DR 16B
Frame Buffer Controller	FB/HC
Video Output	VO 30 or VO 24/HS
Host Interface	
Card Cage and Power Supply	CB 20

Each DR 16B can be accessed as 512x512x2 or 1024x512x1. Minimum configuration contains one DR 16B. Maximum configurations are 512x512x32 and 1024x1024x24. CB 20 holds 20 cards. Multiple cage configurations are possible.

The basic configuration does not include a color look-up table. One of the modules LU 24, LU 24/HS, LUI 24 should be chosen to add color look-up capability.

The Video Input Module, VI 8 (2 cards), can be added to the system to provide real time storage of video signals.

Mass Image Storage, KD 64, can be added to the system to store unencoded images or, for real time playback, run-length encoded images.

Animation capability can be added to the system by including the Mass Image Storage and one of the modules RLI 8, RLS 10, or RLU 24 for run-length decoding.

BASIC PROCESSOR CONFIGURATION:

	<u>Module Name</u>
Processor	BMP 32
Microprogram Sequencer	MPS 16
Microprogram Memory (2 cards required)	SR 1, SR 4, or SR 8
Processor Working Memory	SR 1, SR 4, or SR 8
Frame Buffer System (from above)	

The processor is designed to be used in a system that contains a frame buffer. All system timing is developed by the Frame Buffer Controller, FB/HC.

Additional processing power may be added by including one or more Multiplier-Accumulator Modules, MA 1024. Multiple MA 1024's may be programmed to run in parallel.

ROM modules may be added to the system to provide look-up tables and/or firmware storage.